Amendments to the Claims:

Please cancel claims 2-24.

Listing of Claims:

1. (Original) A cache memory system for storing blocks of graphics data in a graphics processing system, the cache memory system comprising:

a first memory to store an address and an associated ID number for each block of graphics data stored in the cache memory, the address corresponding to a storage location in the cache memory and the associated ID number assigned to distinguish between blocks of graphics data having the same address;
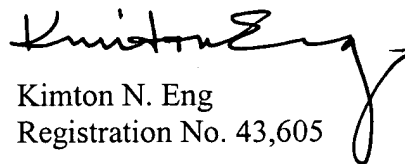
a comparator coupled to the first memory to generate a signal in response to receiving a requested address and ID number for graphics data matching one of the addresses and associated ID numbers stored in the first memory; and

a second memory coupled to the comparator to provide the block of graphics data corresponding to the matching address and associated ID number in response to receiving the signal.

2-24. (Cancelled)

Respectfully submitted,

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